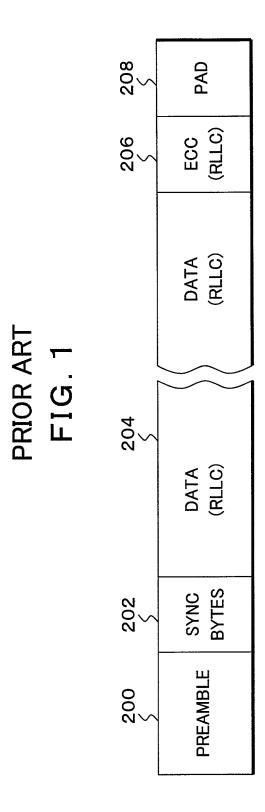
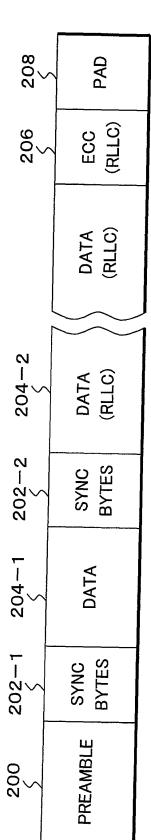
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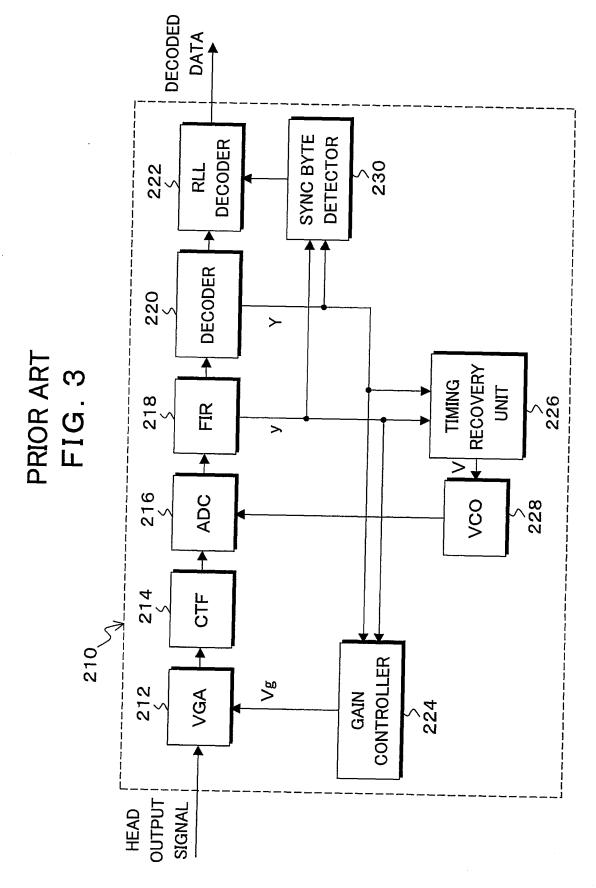


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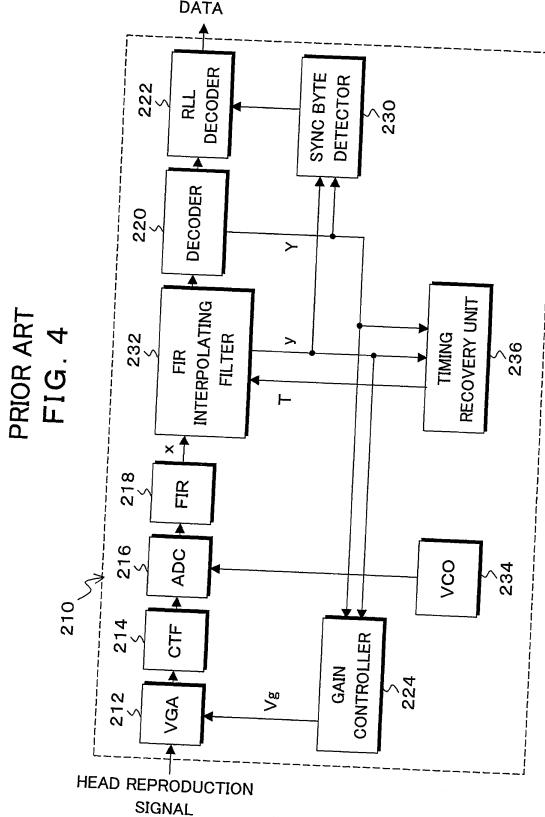
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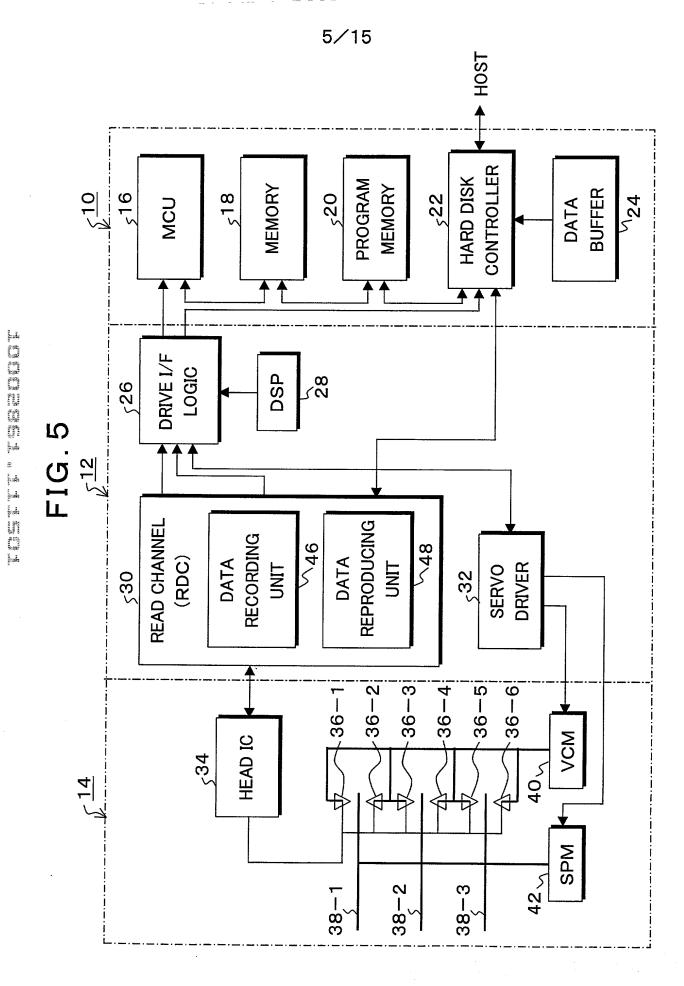
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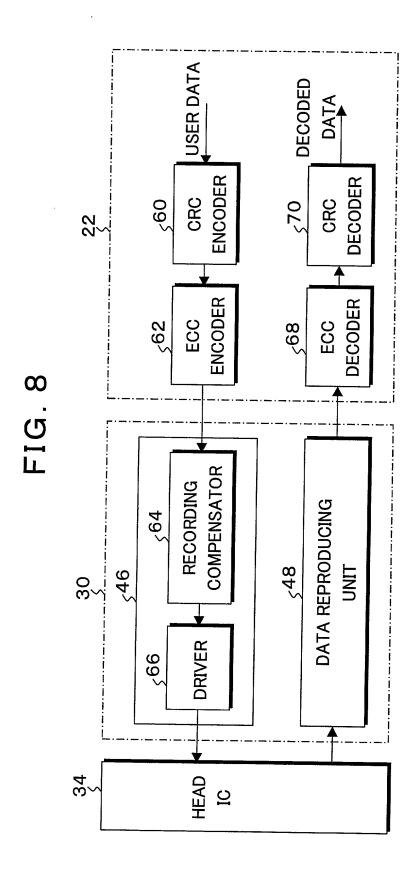
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50-n <	REVISE BYTES				
ან∼	DATA				
50-(n-1)	REVISE BYTES				
54-m	) DATA				
50-3 5	REVISE BYTES				
54–2 >	DATA				
50-2 50-2	REVISE BYTES				
54-1	DATA				
$\sim$	SYNC BYTES				
50-1	REVISE BYTES				
1 52 ~	SYNC				

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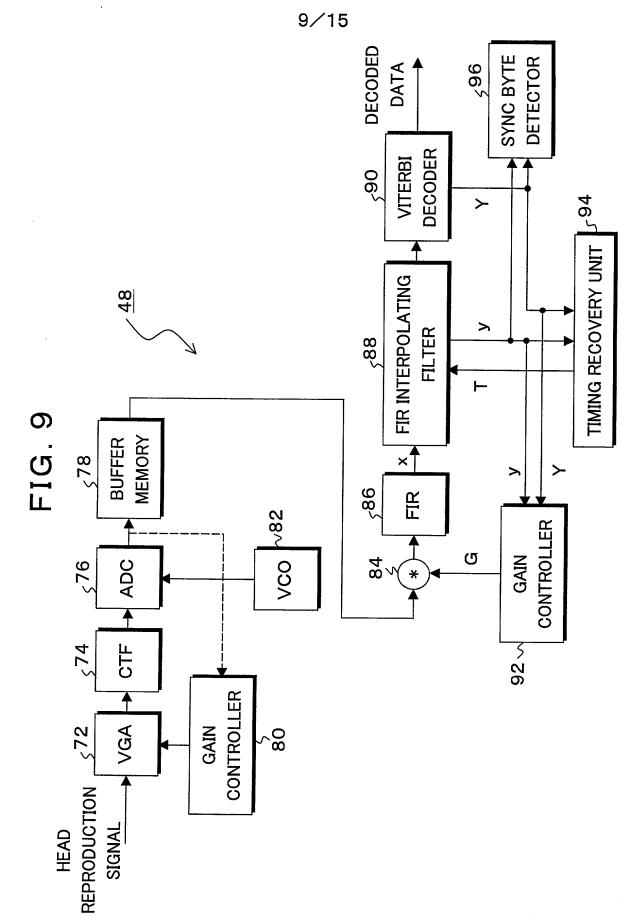
		•				
58-n >	REVISE BYTES		SYNC	BYTES		
~ 20 ~	DATA ECC					
58—(n-1)	REVISE BYTES	•	SYNC	BYTES		
54-m	DATA					
$\begin{array}{cccc} 58-3 & 54-3 \\ & & & \\ & & & \\ & & & \\ \end{array}$	DATA					
58-3 <	REVISE BYTES	•	SYNC	BYTES		
54-2 54-2	DATA					
58-2 <	REVISE BYTES	•	SYNC	BYTES		
54-1 >	DATA					
58-1 \	REVISE BYTES	•	SYNC	BYTES		

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FIG. 10

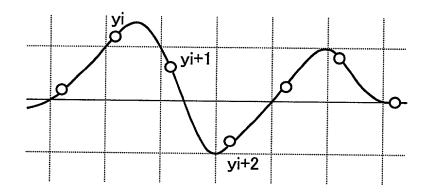
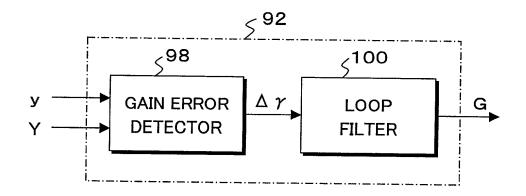
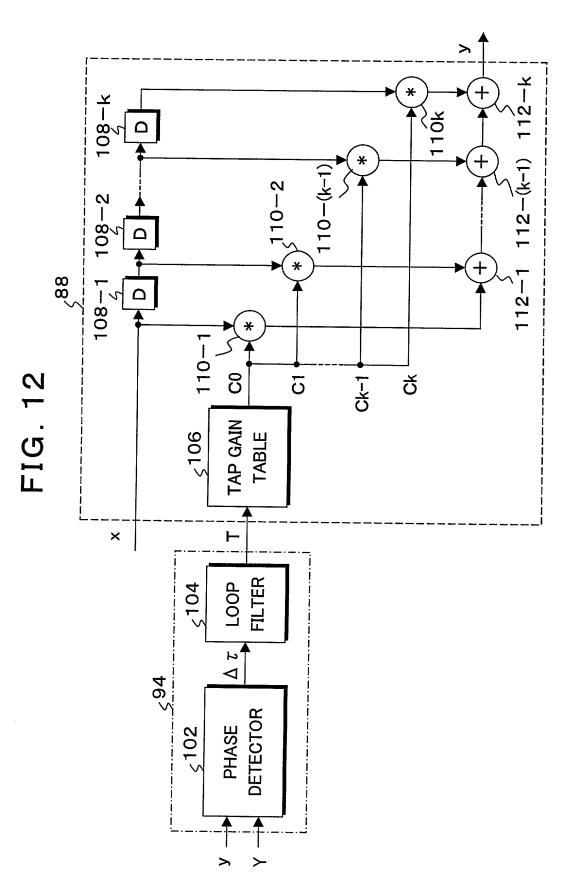


FIG. 11



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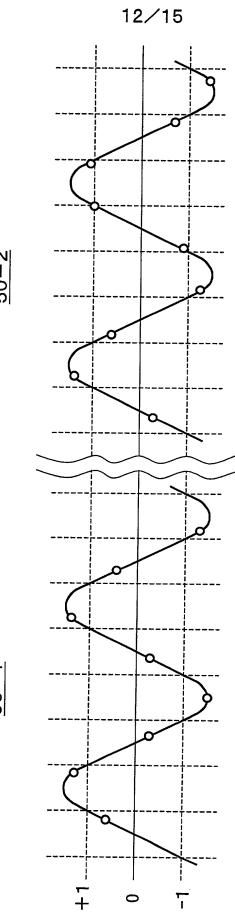
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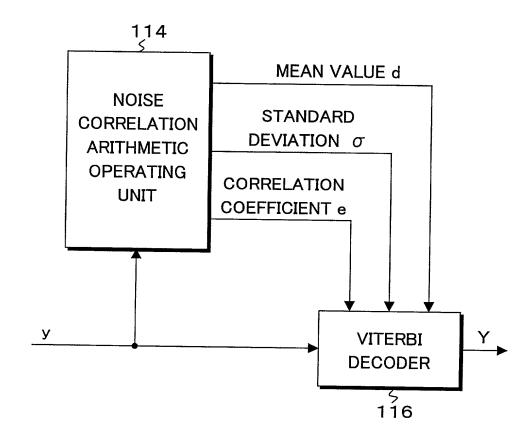
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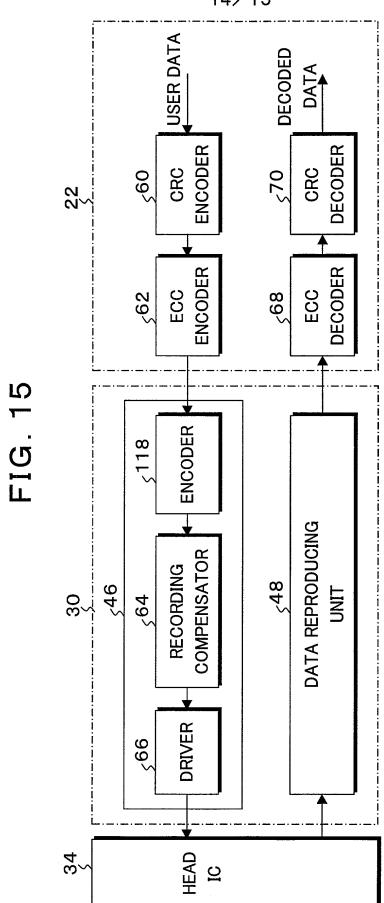
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FIG. 14

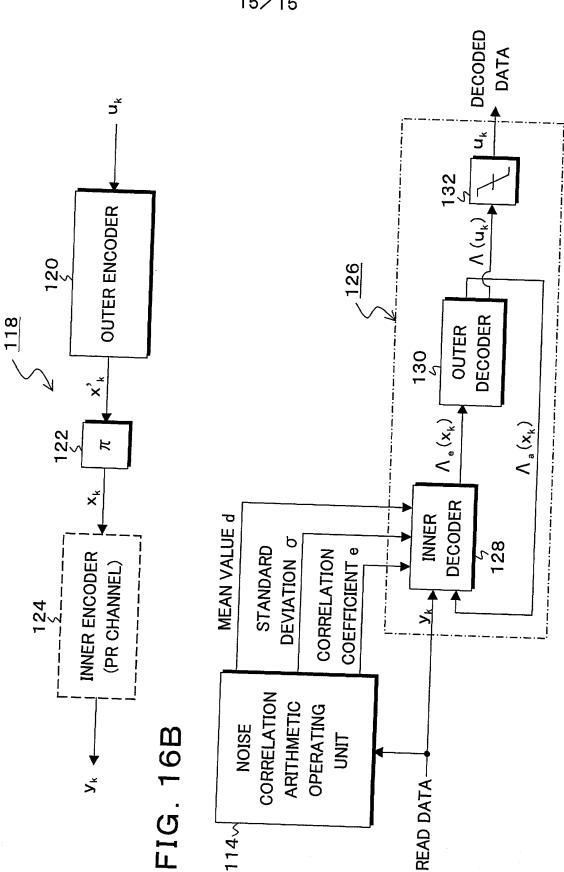


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FIG. 16A